

## CLAIMS:

1. An integrated tuner comprising:  
a step AGC amplifier (1); and  
means (7-11) for adjusting the step AGC amplifier (1) only during a vertical  
synchronization interval.  
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2. An integrated tuner as claimed in claim 1, wherein the adjusting means (7-11)  
comprise:  
a clock generator (7) for generating clock pulses;  
an up/down counter (11) for generating control signals to adjust the step AGC  
10 amplifier (1);  
means (8) for passing said clock pulses to said up/down counter (11) only  
during said vertical synchronization interval.
3. An integrated tuner as claimed in claim 2, wherein the adjusting means (7-11)  
15 further comprise:  
a level detector (9, C1) coupled to an output of the step AGC amplifier (1);  
and  
a dual comparator (10) coupled to an output of said level detector to provide  
up/down control signals to said up/down counter (11) in dependence on an output signal of  
20 said level detector (9, C1).
4. An integrated tuner as claimed in claim 3, wherein the level detector (9, C1)  
continuously measures a total power of all signals in all channels applied to the step AGC  
amplifier (1).  
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5. A receiver comprising:  
an integrated tuner as claimed in claim 1; and  
an IF demodulation circuit (5,6) for providing a vertical sync signal to the  
integrated tuner.